OPTION SHEET FOR NANODOCK SDR

Reset Options

Customer Product ID: _____ (optional, enter your reference here)

Order number: _____

Example Use:

Option A	✓	= Yes
Option B		= No

1. Configuration Table

Number of mounted units on the NanoDock SDR – See chapter 2 Place one mark in each row				
	0 pcs	1 pcs	2 pcs	3 pcs
NanoMind Z7000				
NanoCom TR-600				

Stack Connector Options - See chapter 3						
	A	B	C	D	E	None
Connector soldered to PCB						
Connector stacked on top						

Clock Source – See chapter 4	
Individual clock on TR-600	✔
Clock distribution	

Clock Sync (GPS PPS) in Stack – See chapter 5	
In stack (H1_2)	✔
Not connected	

CAN through stack connector – See chapter 6	
Through stack (H1_1 and 3)	✔
Not connected	

CAN termination resister - See chapter 7	
120 ohm	✔
Not mounted	

I^2C through stack connector – See chapter 8	
Through stack (H1_41 and 43)	✔
Not connected	

Choose 5.0 V VCC channels in stack – default not in stack – See chapter 9	
H1_47	
H1_48	
H1_49	
H1_50	
H1_51	
H1_52	
H2_1	
H2_2	
H2_3	
H2_4	
H2_5	
H2_6	
H2_27	
H2_28	
H2_49	
H2_51	

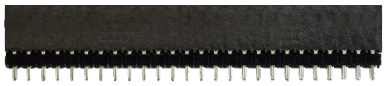
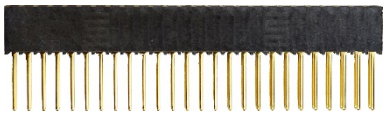
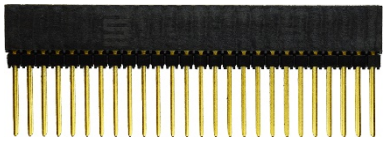
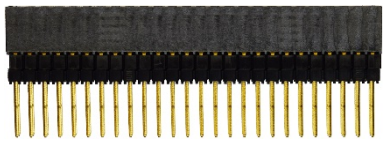
Choose ground in stack – default not in stack – See chapter 9	
H1_45	
H1_46	
H2_7	
H2_8	

2. Mounted Systems

Place one mark in each row, depending on the number of subsystems there is to be mounted on the NanoDock SDR. Note that the Z7000 can only be mounted in one slot and that the TR-600 is mounted in the top side first next to the Z7000, and then on the bottom starting with the slot below the already mounted TR-600.

3. Stack Connectors

The following types of Samtec connectors are the available types for this product. If another connector is needed, please contact GomSpace to get a quote for mounting another type.

A		ESQ-126-12-S-D
B		SSQ-126-03-G-D
C		ESQ-126-14-G-D
D		ESQ-126-24-G-D
E		SSQ-126-01-F-D

4. Clock

Default is each NanoCom TR-600 uses its own clock. By marking the box the clock distribution system described in the datasheet chapter 2.2.1 is applied.

5. Clock PPS from GPS

A PPS from another subsystem can supply a signal (3.3 V input) through the stack to synchronize the clock. By marking the box no signal is transferred to the NanoDock SDR.

6. CAN

Per default there is CAN access through the stack. By marking the box the CAN access is removed. Note CAN is still accessible on connector J22.

7. CAN Termination Resister

View datasheet chapter 2.2.7.

8. I2C

Per default there is I^2C access through the stack. By marking the box the I^2C access is removed. Pulled to 3.3 V though 200k Ω.

9. 5 V Supply and GND

Mark which stack connector pins supplies the NanoDock SDR with power. Note that it is advised to use connector J16 to power the unit. See datasheet chapter 3

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